

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 10-053,198	GROUP/ART UNIT 2817	ATTACHMENT TO PAPER NUMBER 3
NOTICE OF REFERENCES CITED				APPLICANT(S) Senthikumar et al.		

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*	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE			
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C	5848253	12-8-1998	Walsh et al.	395	307				
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L	200003174	01-28-2000	Japan	Okutsu et al.	—	—			
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R	Vittor et al. "CMOS Analog Integrated Circuits Based on Weak Inversion Operation" IEEE Journal of Solid-State Circuits, June 1977 pp 224-231
T	Millman "Microelectronics" McGraw-Hill Book Co 1979 pp 237
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EXAMINER MICHAEL B SHINGLETON	DATE 5-5-03	
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* A copy of this reference is not being furnished with this office action.
(See Manual of Patent Examining Procedure, section 707.05 (a).)